

REMARKS/ARGUMENTS

In the application as originally filed, the respective descriptions of Figs. 1 and 2 were inadvertently combined, creating unclearness. As filed, the application suggested that Fig. 2 was prior art, which was incorrect. Pages 2, 3, 4 and 5 are being amended herein. Basically, the description of Fig. 2 is being moved from the Background section to the beginning of the Detailed Description. These amendments are supported by the first provisional priority application, serial no. 60/425,422 filed November 11, 2002 (the "422 provisional").

Enclosed is a photocopy of the '422 provisional. The '422 provisional has two block diagrams, Figs. 1 and 2, and one page of text, as well as a photo (Fig. 3) and a graph (Fig. 4) illustrating the proposed invention.

Fig. 1 is described as being prior art. It is labeled "Traditional" and described as prior art in the second sentence in the '422 provisional, as follows: "Traditionally, multiple isolated bricks were used to power various low voltage loads on the board." Note that there are multiple "Isolated BMP ['Board-Mounted Power'] Modules" in Fig. 1, corresponding to the "multiple isolated bricks" in the text.

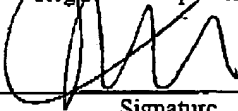
On the other hand, Figure 2 in the '422 provisional is described as being an embodiment of the invention. The drawing caption states "Proposed on-board power distribution optimized for performance and simplicity." The text of the '422 provisional describes Figure 2 as an invention, starting with the following sentences (in paragraph 1): "In the new approach, each system board has a single isolated dc-dc converter [shown in Fig. 2] that is used to convert nominal 48V input [shown in Fig. 2] into a tightly regulated intermediate bus voltage [6V-10V in Fig. 2]. Multiple non-isolated POL converters [shown in Fig. 2] are then used to power various loads [shown in Fig. 2] on the motherboard from the intermediate bus." Thus, the '422 provisional teaches that Fig. 2 is an embodiment of the invention.

In summary, Fig. 2 in the '422 provisional, and the corresponding text, should not have been described in the Background section of this application. The present amendment is supported by the '422 provisional and rectifies this error.

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office at facsimile No. (703) 872-9306, on February 8, 2005:

James A. Finder

Name of applicant, assignee or
Registered Representative



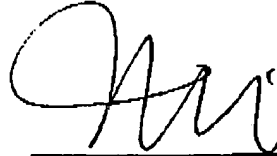
Signature

February 8, 2005

Date of Signature

JAF/BAM:lac

Respectfully submitted,



James A. Finder

Registration No.: 30,173

OSTROLENK, FABER, GERB & SOFFEN, LLP

1180 Avenue of the Americas

New York, New York 10036-8403

Telephone: (212) 382-0700